US-PAT-NO:

6835956

DOCUMENT-IDENTIFIER: US 6835956 B1

TITLE:

Nitride semiconductor device and manufacturing method

thereof

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US Patent No. - PN (1): 6835956

US Document Identifier - DID (1): US 6835956 B1

Detailed Description Text - DETX (29):

(<u>Undoped n-type Contact Layer</u> 1:Al.sub.a G.sub.1-a N of the Present Invention)

Detailed Description Text - DETX (30):

An <u>undoped n-type contact layer</u> 1 made of undoped Al.sub.0.05 Ga.sub.0.095 N was grown to the thickness of 1 .mu.m on the GaN substrate 30, using TMA (trimethylalminium), TMG and ammonia gas as a source gas at 1050.degree. C. (n-type contact layer 2: Al.sub.a Ga.sub.1-a N of the present invention)

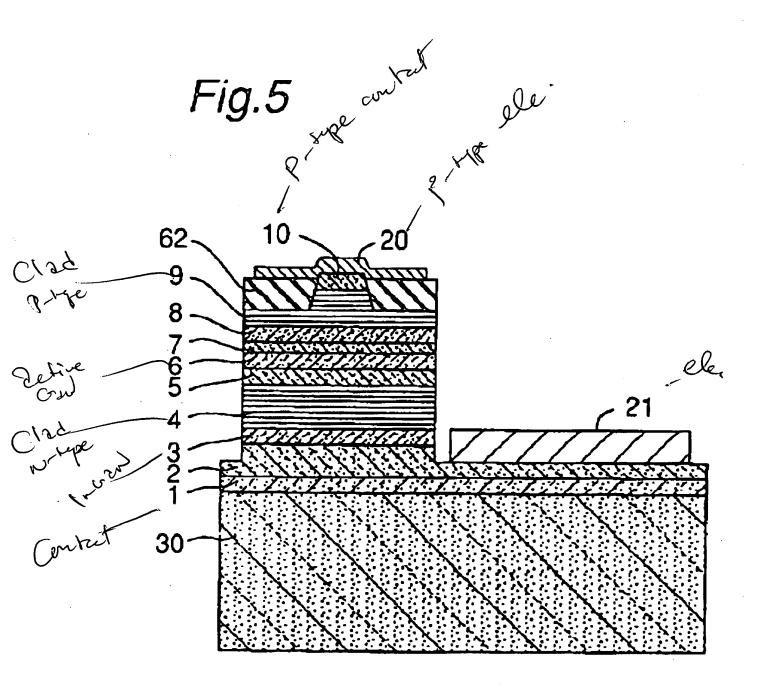
Detailed Description Text - DETX (32):

Now, there were no small cracks in the above-mentioned n-type contact layer (including the n-type contact layer 1), thus the cracks can be prevented well. If there are any small cracks in tha GaN substrate, the small cracks can be prevented from propagating by growing the n-type nitride semiconductor layer 2, so as to grow a device structure having a good crystallinity. The crystallinity was enhanced better in the case that both an n-type contact layer 2 and an <u>undoped n-type contact layer</u> 1 are formed as described above, compared with the case that only the n-type contact layer 2 was formed.

Detailed Description Text - DETX (63):

The laser device was fabricated in the same manner as in Example 1, except that the <u>undoped n-type contact layer</u> 1 was not grown and only the n-type contact layer 2 was grown.

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(12) United States Patent

Nagahama et al.

(10) Patent No.:

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(45) Date of Patent:

Dec. 28, 2004

(54) NITRIDE SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

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(*) Notice: Subject to any disclaimer, the term of this

patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

(21) Appl. No.: 09/500,288

(22)

Feb. 8, 2000 (30)Foreign Application Priority Data

| (51) Int CL ⁷ | | T | U01T 2 | 7/15 |
|--------------------------|------|---|---------|------|
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| Feb. 9, 1999 | (JP) | | P 11-03 | 0990 |

U.S. Cl. 257/79; 257/13; 257/82; 257/85; 257/94; 257/101; 257/103

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ABSTRACT (57)

A nitride semiconductor device includes a GaN substrate having a single-crystal GaN layer at least on its surface and plurality of device-forming layers made of nitride semiconductor. The device-forming layer contacting the GaN substrate has a coefficient of thermal expansion smaller than that of GaN, so that a compressive strain is applied to the device-forming layer. This result in prevention of crack forming in the device-forming layers, and a lifetime characteristics of the nitride semiconductor device is improved.

8 Claims, 4 Drawing Sheets

